

CLAIM SET AS AMENDED

1. (currently amended) A process for ~~the~~ metrological analysis of multicarrier signals, ~~characterised in that~~ wherein the I- and/or Q-components acquired by demodulation are integrated over a plurality of values and are graphically displayed on ~~the~~ a screen of a display device for each individual carrier or combined carrier group of the multicarrier signal in that the individual carriers or carrier groups are plotted one beside another on ~~the~~ a horizontal axis of a diagram and the I- and/or Q-components associated with these carriers or carrier groups are in each case plotted along a vertical line.

2. (currently amended) [[A]] The process according to Claim 1, ~~characterised in that~~ wherein the I- and Q-components associated with each individual carrier or carrier group are projected onto a single vertical line so that the I- and Q-components for all the carriers occur in a horizontal line of the diagram and faulty I- or Q-values appear as vertical lines or entirely absent I/Q-values appear as gaps in the horizontal lines.

3. (currently amended) [[A]] The process according to Claim 1, ~~characterised in that~~ wherein in each case only a portion of the total frequency band occupied by the multicarrier system is

displayed on the screen.

4. (currently amended) [[A]] The process according to claim 1, ~~characterised in that~~ wherein the integration and line projection of the I- and Q-values of the individual symbols, acquired by complex demodulation of the individual carriers, take place in a matrix memory which in one of its dimensions has as many row addresses as I- and Q-values are provided for the coding used, and in its other dimension has as many column addresses as individual carriers or carrier groups combined from adjacent carriers are provided, and wherein, consecutively for each carrier or each carrier group, a hold value is in each case entered in the row addresses, corresponding to the respective I- and Q-values, of the associated column addresses, and following the analysis of a predetermined number of symbols, the content of the matrix memory is read out for each column address for the graphic display on the display device.

5. (currently amended) [[A]] The process according to Claim 4, ~~characterised in that~~ wherein following the reading out of the matrix memory, the content of all the addresses of the matrix memory is decremented by 1.